

Inventor(s): Pan
Serial No.: To Be Assigned
Filed: Herewith
For: Semiconductor Wafer Tray Positioning
Attorney Docket No.: 67,200-627

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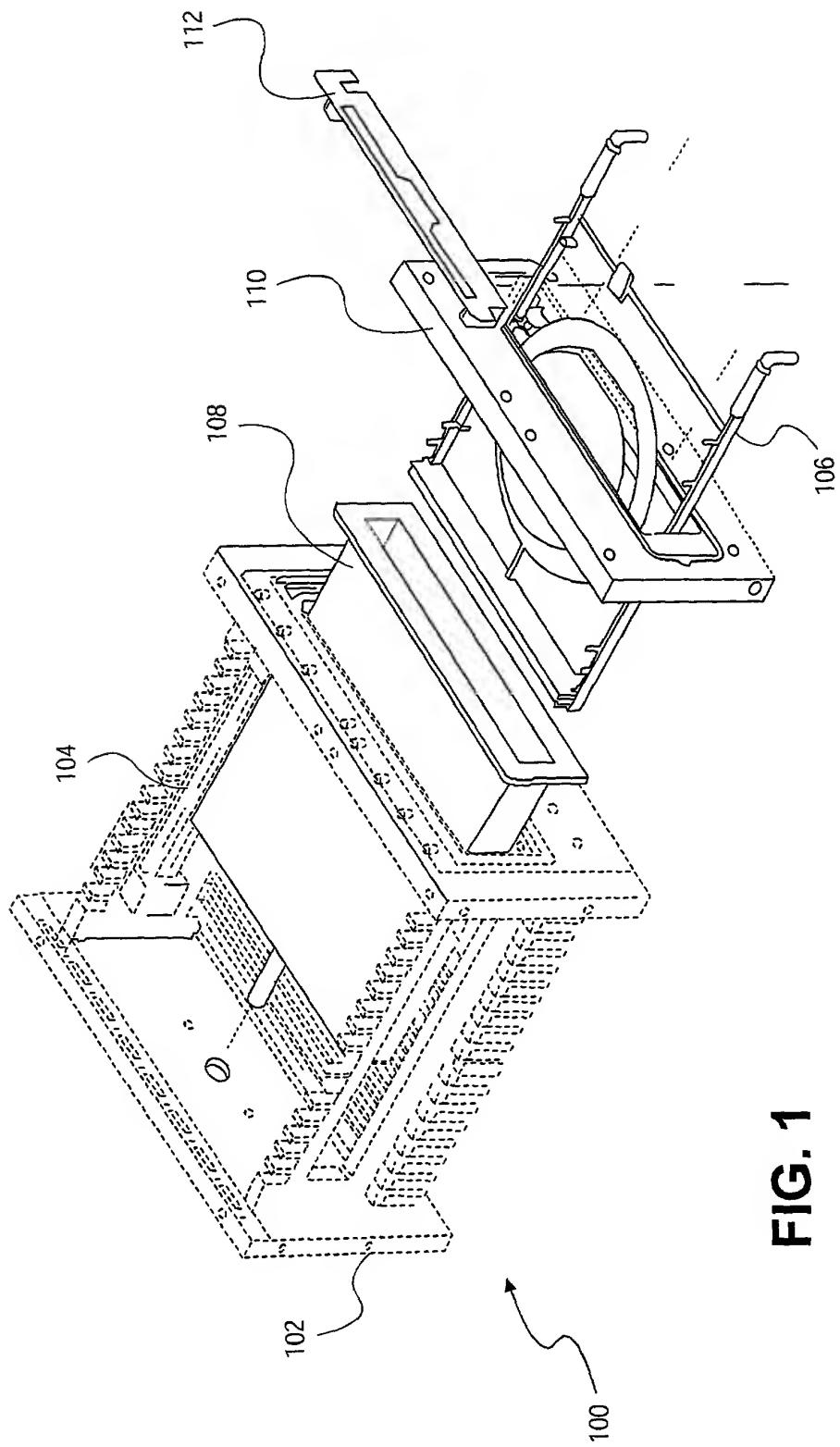


FIG. 1

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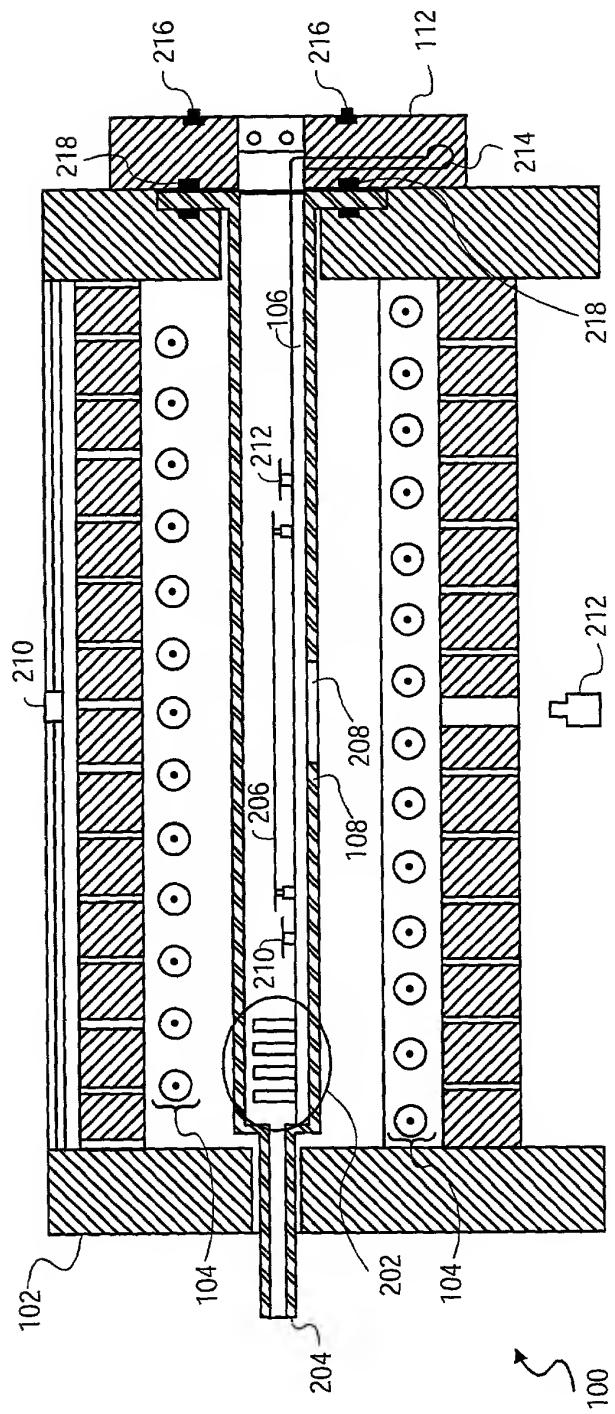
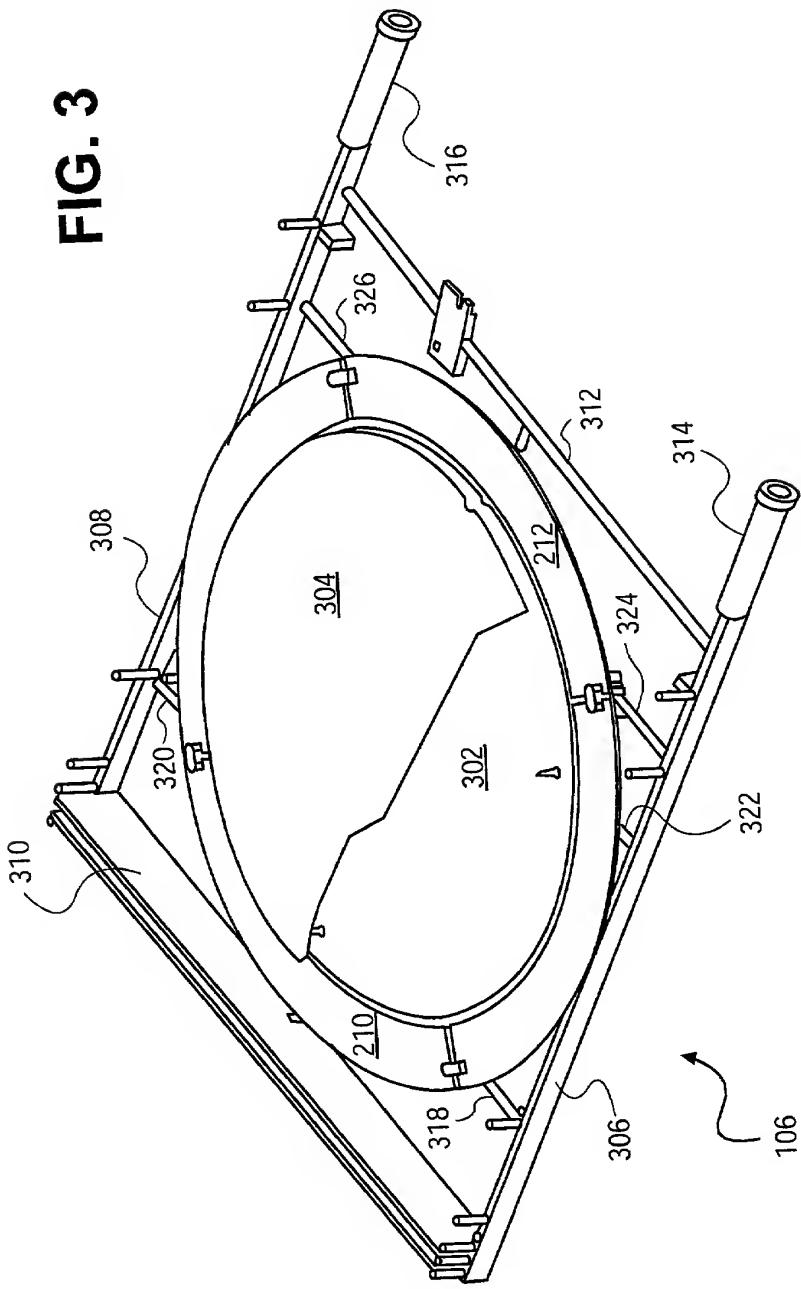


FIG. 2

FIG. 3



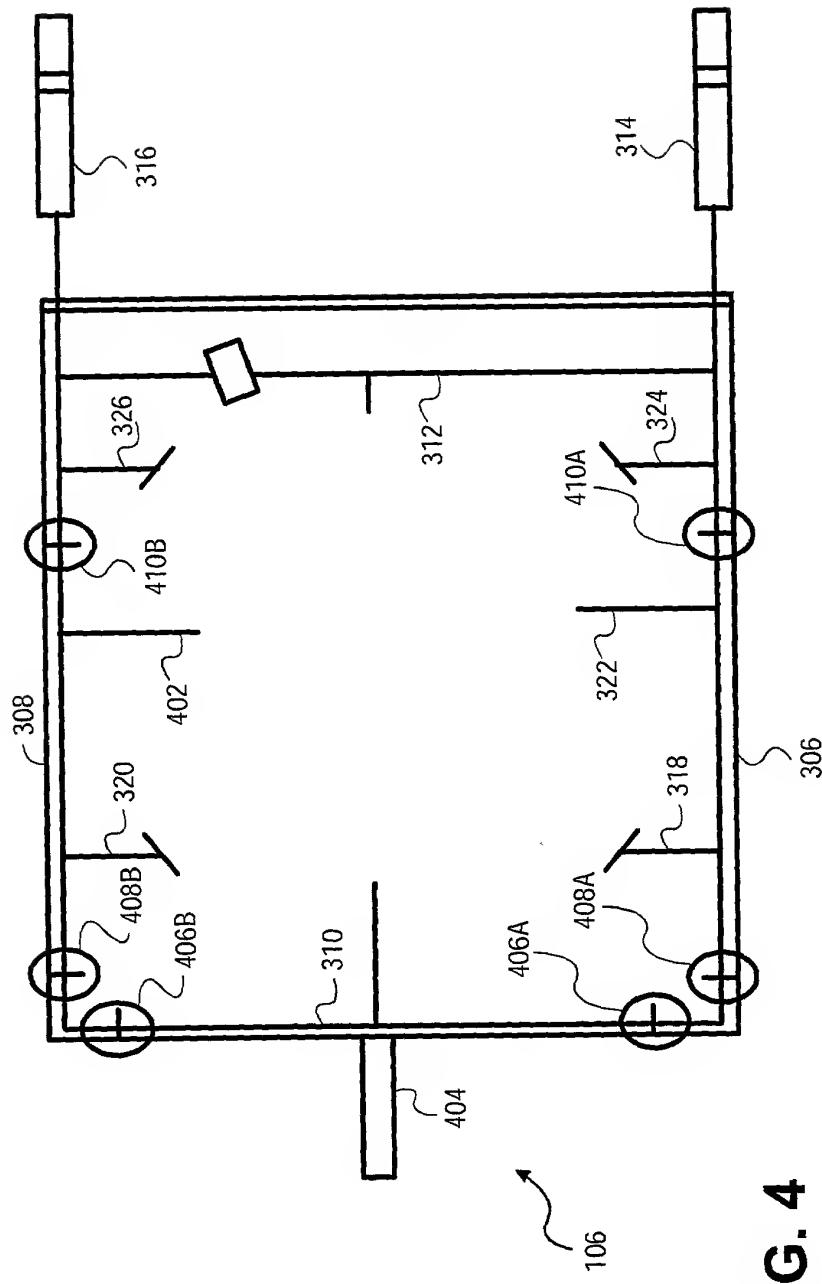


FIG. 4

FIG. 5B

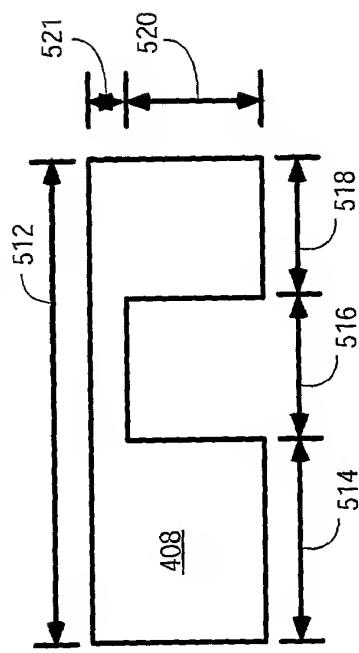


FIG. 5A

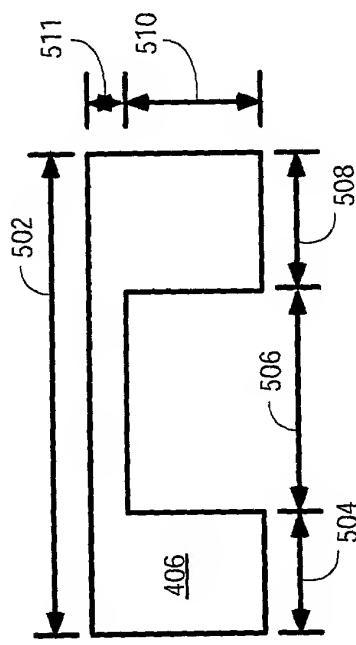
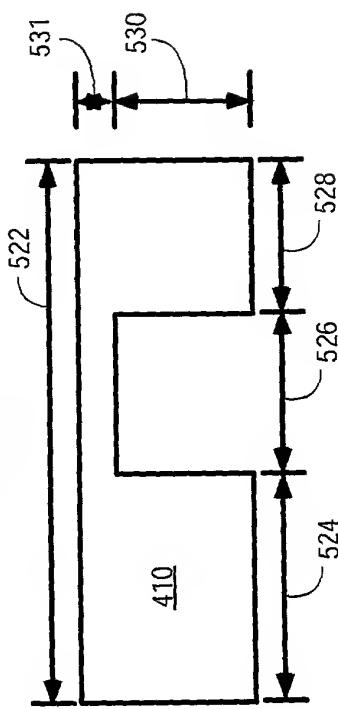


FIG. 5C



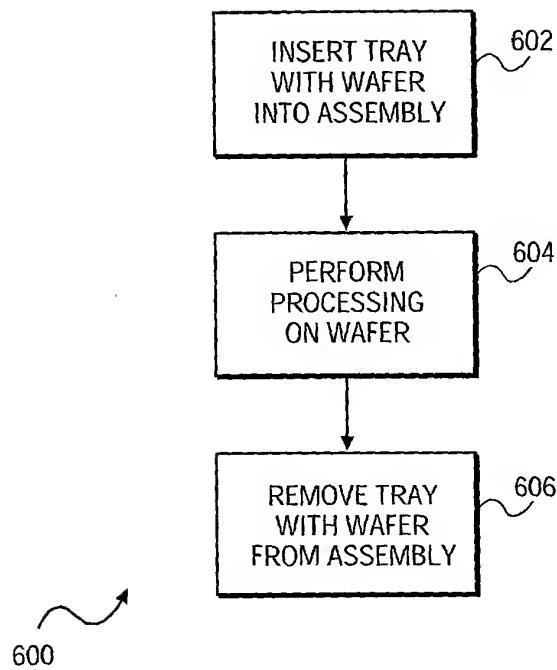


FIG. 6